



ABSTRACT OF THE DISCLOSURE

A memory interface device has a write detection section that detects the write of a predetermined unit amount of data by a memory write unit into a memory. A signal generation section generates a signal to notify the memory write unit that readout of data from the memory by a memory readout unit has been completed. A data storage amount measurement device measures an amount of data stored in the memory during the memory write procedures. A memory readout control section generates an interrupt signal with respect to the memory readout unit when the stored data amount in the memory reaches a predetermined readout start storage amount. A timer counts a period in which writing of the predetermined unit amount of the data into the memory by the memory write unit is discontinued and outputs a timeout signal to the memory readout control section when a value of the period count reaches a predetermined timer period. The memory readout control section generates the interrupt signal with respect to the memory readout unit even when the memory readout control section receives the timeout signal output from the timer.